

TIME-SLOT INTERCHANGE SWITCHES HAVING AUTOMATIC FRAME ALIGNMENT MEASUREMENT AND PROGRAMMING CAPABILITY

Abstract of the Disclosure

- Time-slot interchange switches include measurement circuits that automatically measure frame alignment associated with a plurality of multi-frame data streams received by the switch. Internal programming circuits are also provided to convert the frame alignment measurements into frame offsets. Unacceptable frame offsets are also automatically identified using error control circuitry. These measurement and programming circuits streamline frame offset measurement techniques and enable on-chip measurement and conversion of frame delays to frame offsets and programming of frame offset registers (FORs).
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